

TELECO "SIGNAL 260

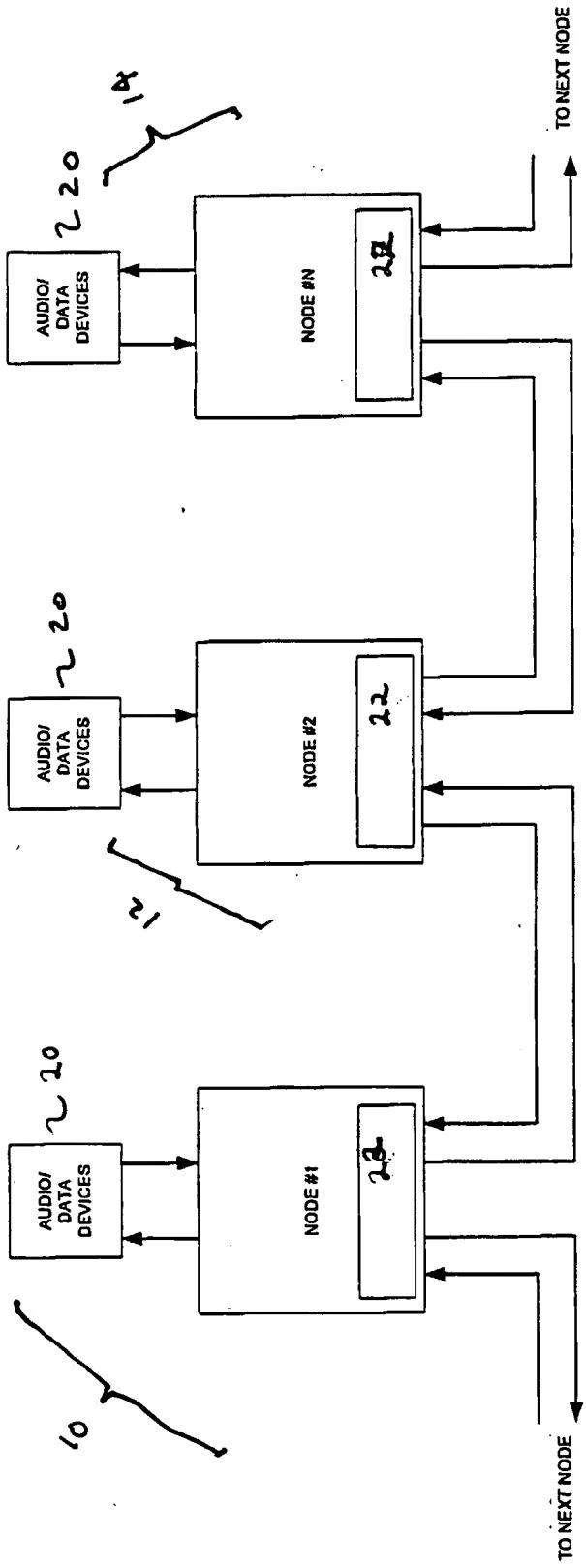


FIG. 1

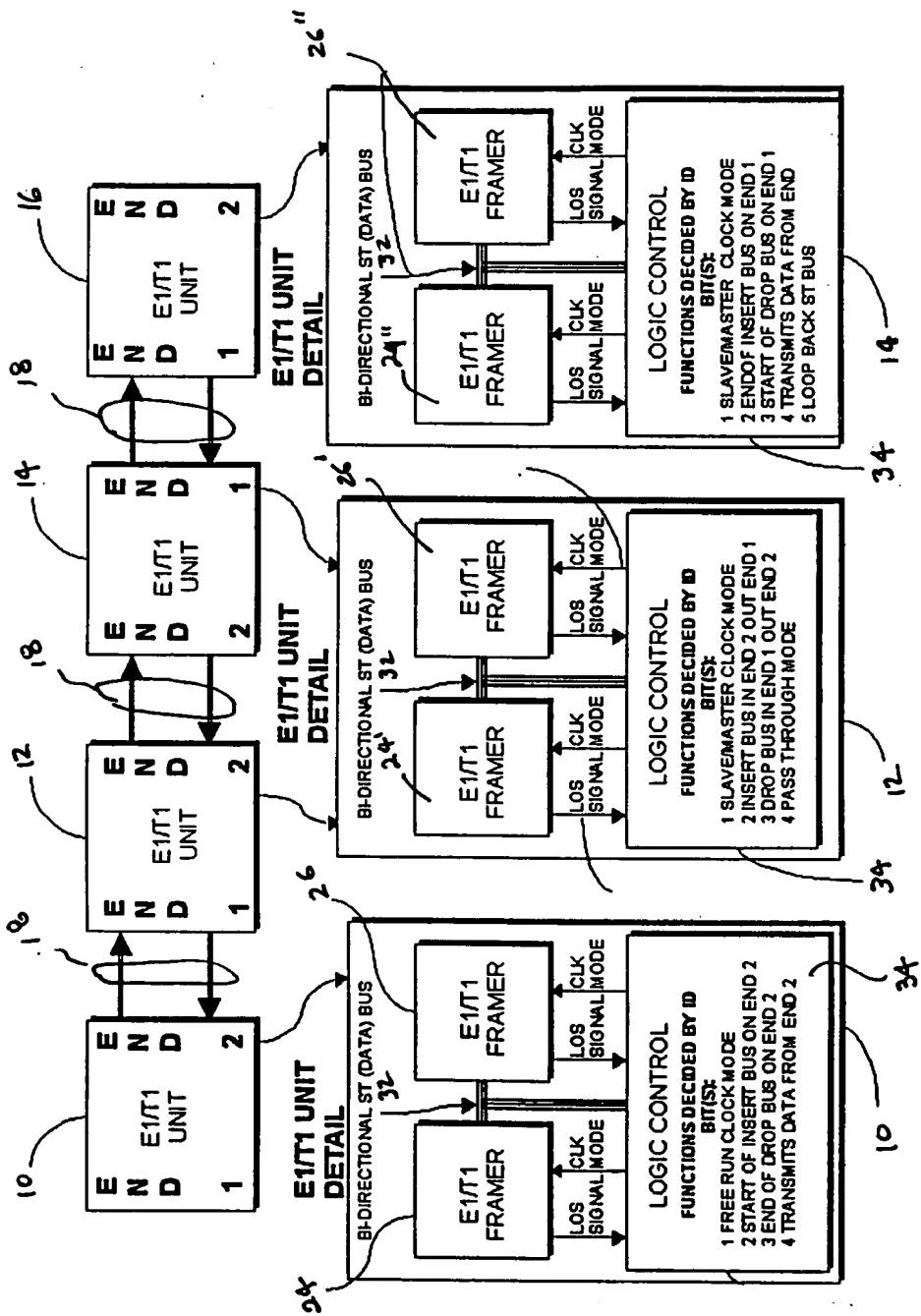
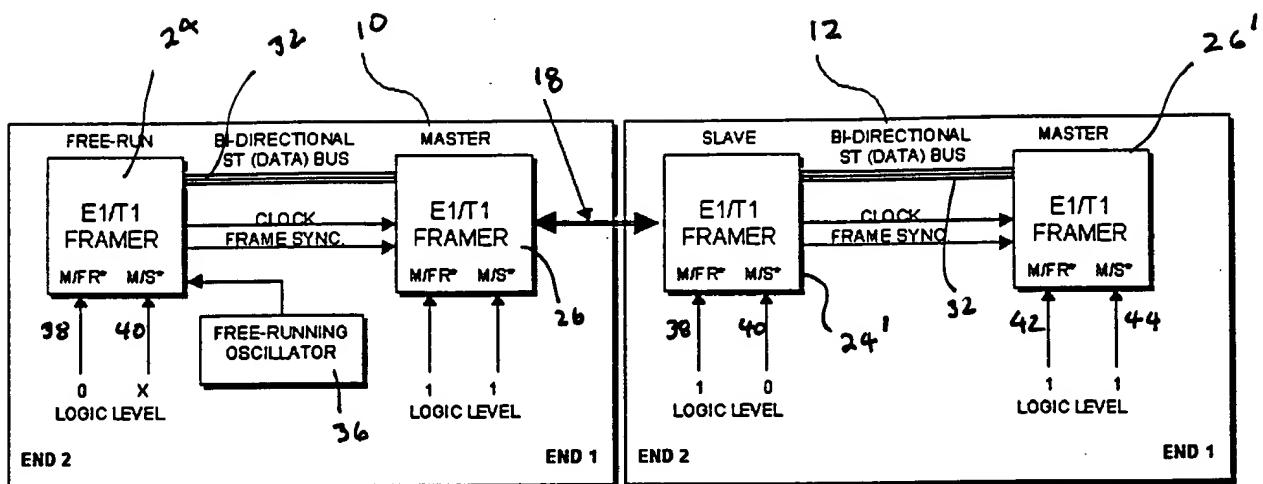


Fig. 2

FIG 3



FRAMER CHIP  
MODE CONTROL  
TABLE

INPUT SIGNAL PIN NAME	FREE RUN	SLAVE	MASTER	MODE
MASTER-SLAVE/NOT FREE RUN	0	1	0	1
MASTER-NOT SLAVE	X	0	X	1
LOGIC LEVEL				

Fig. 3 A

TDE050 "E1/E2/E3/E4"

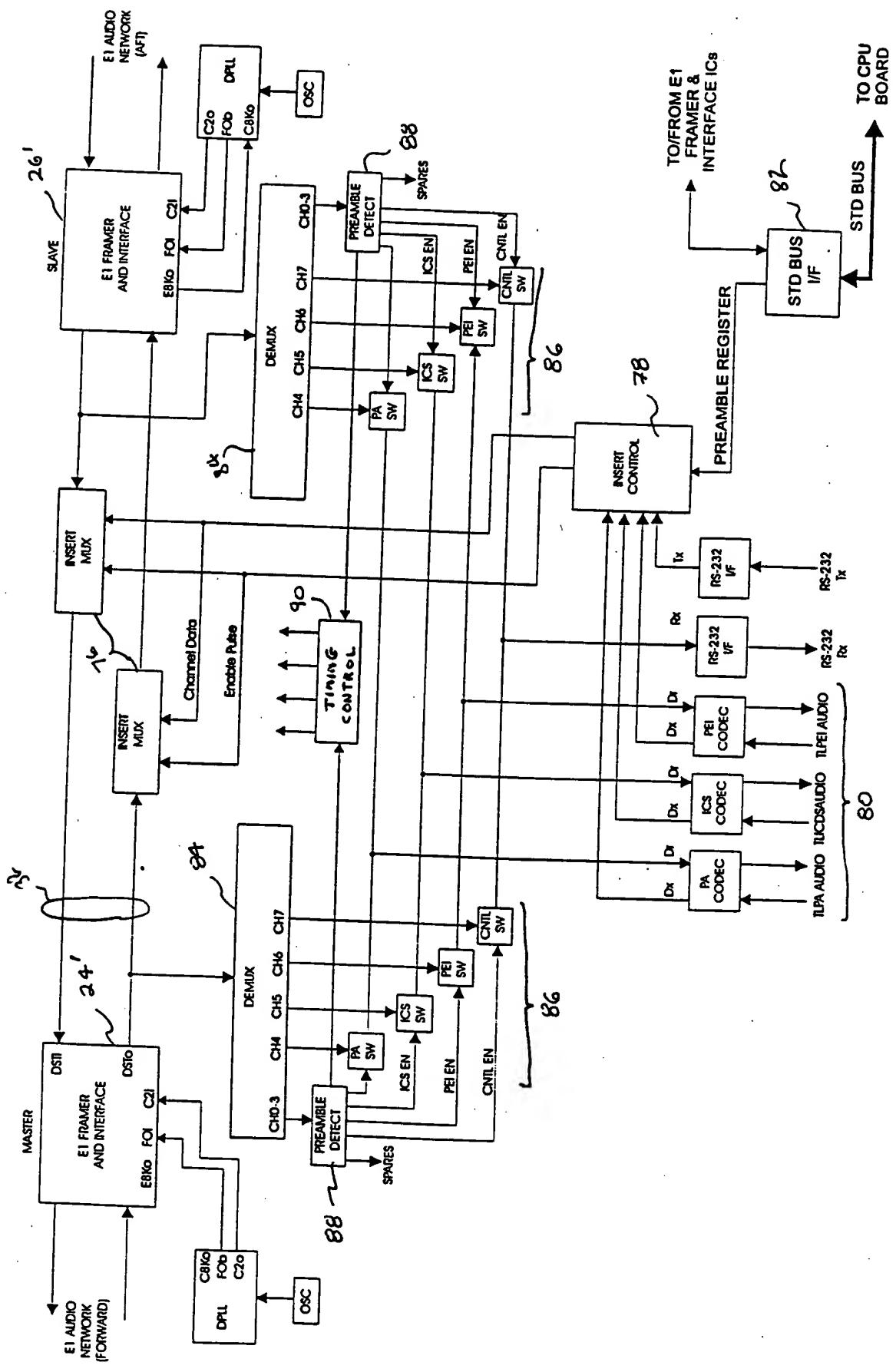


FIG. 4

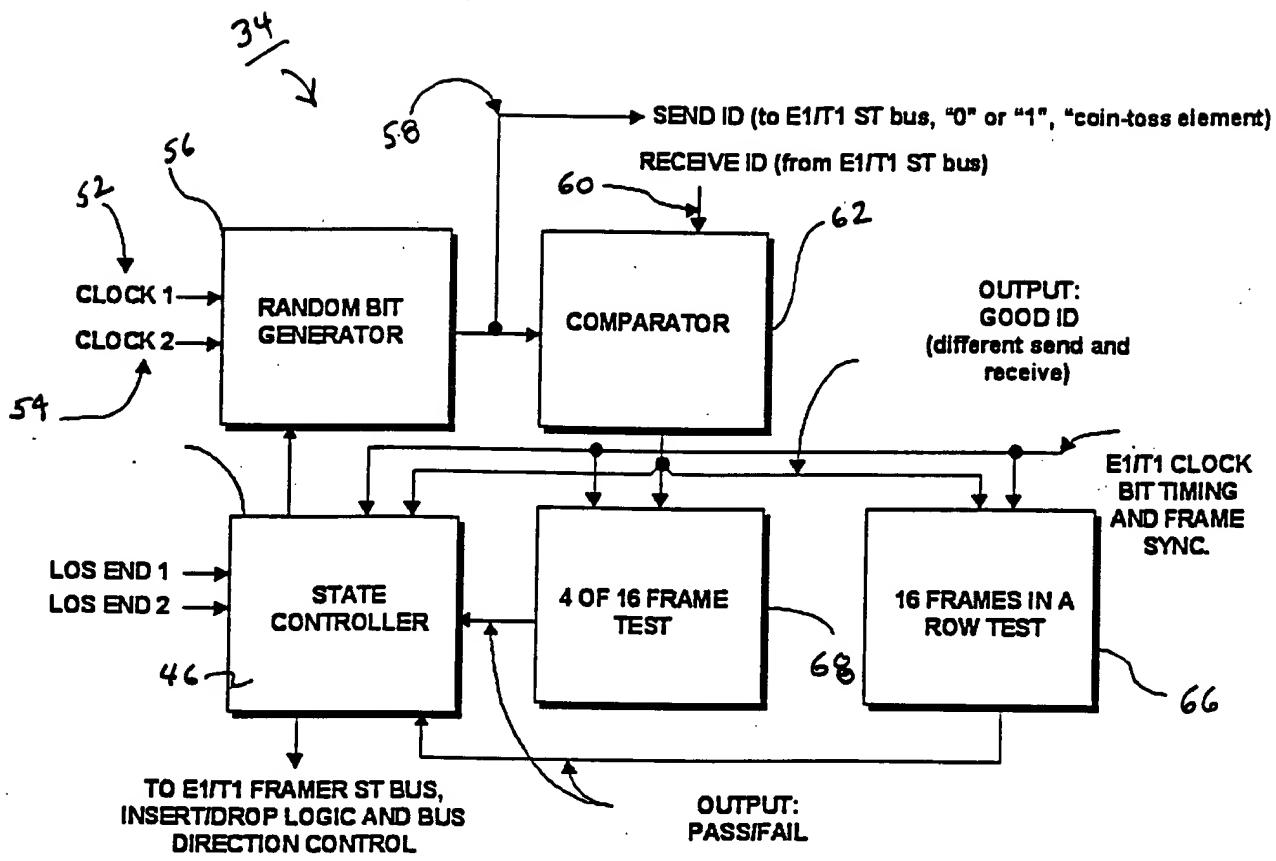


Fig. 5

TE0E50-EE62460

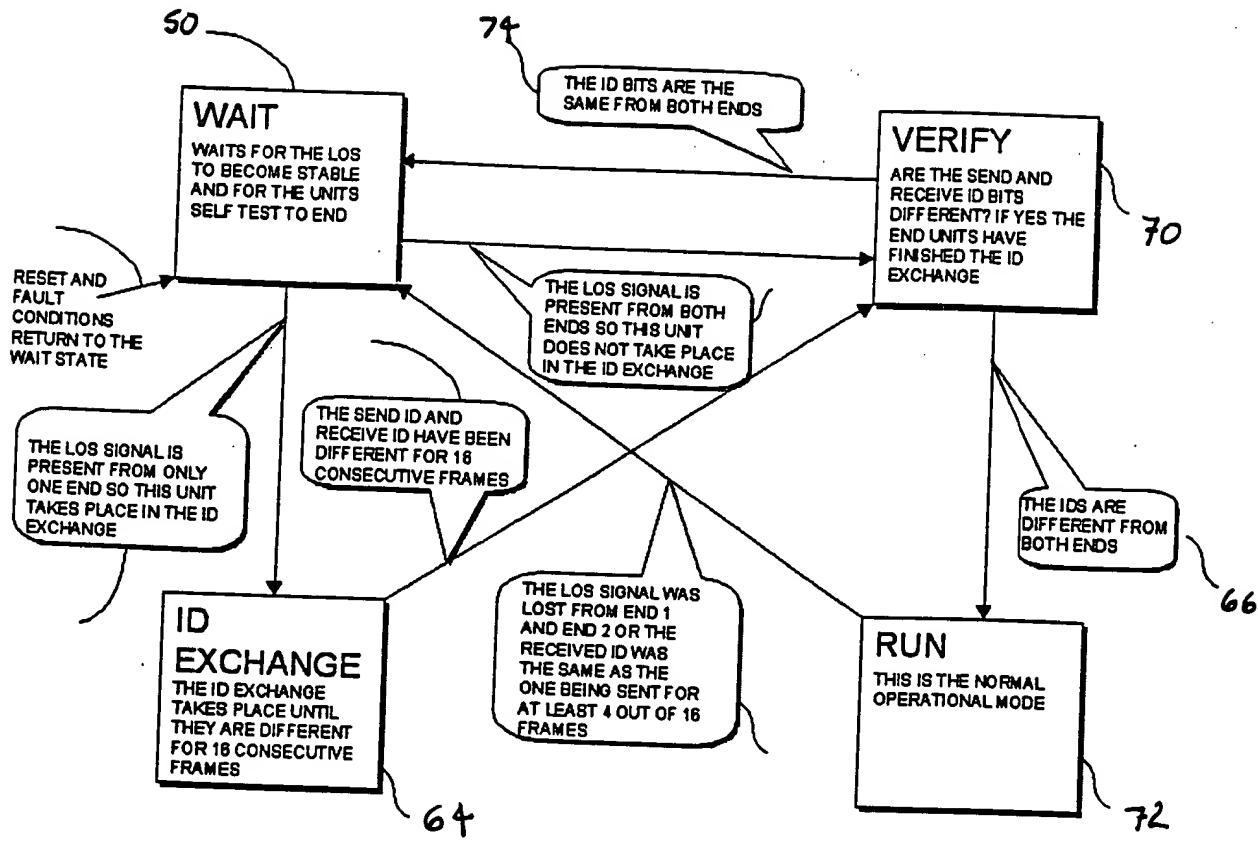


Fig. 6

T02020 "SEGMENT 60

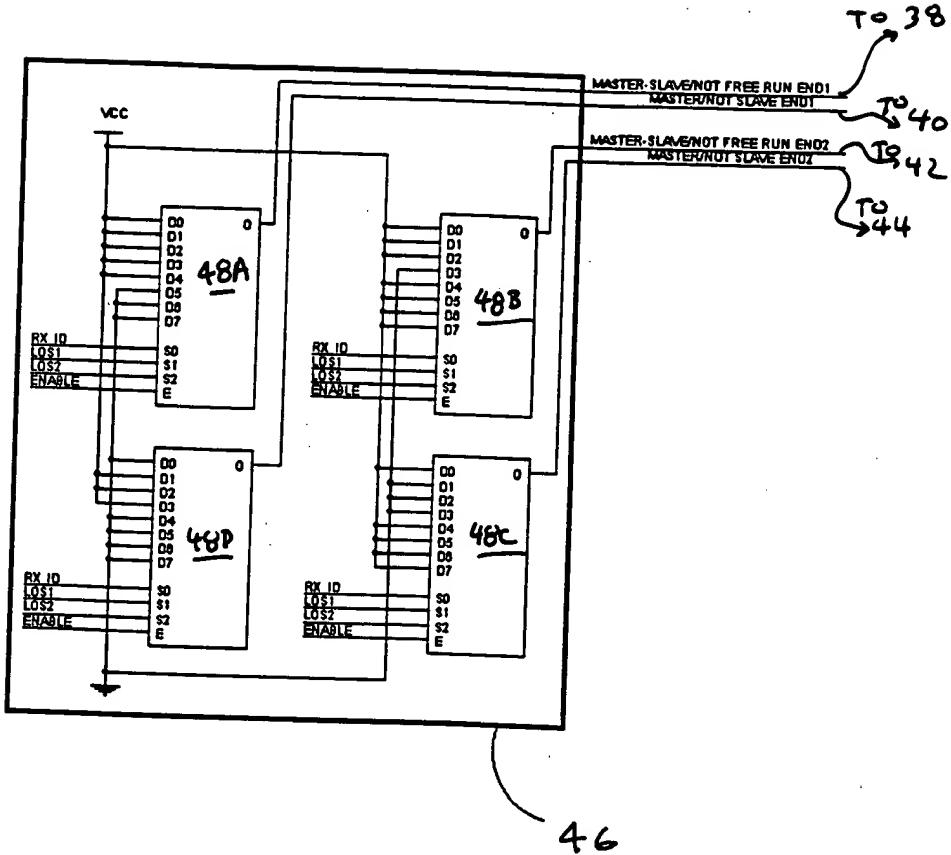


Fig. 7

Fig 7A

Mode Table

<u>LOS1</u>	<u>LOS2</u>	<u>Rx</u>	<u>Address</u>	<u>Master Slave</u>	<u>Free-run</u>
0	0	0	0	end 2	end 1
0	0	1	1	end 1	end 2
0	1	0	2	end 1	end 2
0	1	1	3	end 1	end 2
1	0	0	4	end 2	end 1
1	0	1	5	end 2	end 1
1	1	0	6		
1	1	1	7		